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PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10051639	FILING DATE 01/18/2002	CLASS 257	SUBCLASS 565	GAU 2815	EXAMINER DAD NGUYEN
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2815

Conder

\*\*CONTINUING DATA VERIFIED:

\*\* FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed	<input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO	
35 USC 119 conditions met	<input type="checkbox"/> yes <input type="checkbox"/> no	TI-32536	
Verified and Acknowledged Examiners's initials			

TITLE : ESD improvement by a vertical bipolar transistor with low breakdown voltage and high beta  
U.S. DEPT. OF COMM./PAT. & TM-PTO-436L(Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED		
		Assistant Examiner		Total Claims <input type="checkbox"/> Print Claim for O.G. <input type="checkbox"/>
ISSUE FEE		Primary Examiner		DRAWING Sheets Drawn <input type="checkbox"/> Fig.s Drawn <input type="checkbox"/> Print Fig. <input type="checkbox"/>
Amount Due	Date Paid	Primary Examiner		
TERMINAL		PREPARED FOR ISSUE		
DISCLAIMER		Application Examiner		
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